

E2 (2019-2025) Batch Sem1 Time Table																																						
	Monday							TUE							WED							THR							FRI							SAT		
Class Code	P1	P2	P3	P4	P5	P6	P7	P1	P2	P3	P4	P5	P6	P7	P1	P2	P3	P4	P5	P6	P7	P1	P2	P3	P4	P5	P6	P7	P1	P2	P3	P4	P5	P6	P7			
ECE1	SG1	PSP		AEC	DSP		PSP	DSP		AEC	AEC		CS		AEC	PSP	DLD	DLD		IOT Lab		CS	CS					AEC Lab										
ECE2	SG2	AEC	PSP		DSP		IOT Lab		AEC	PSP	PSP	CS		AEC Lab	DSP		CS	AEC	AEC		DLD Lab		DLD					DSP Lab										
ECE3	SG3		AEC	DLD	DLD		AEC Lab		AEC	DLD	DLD			DLD Lab	DSP	DSP	CS	AEC		DSP Lab		PSP		DLD				PSP	AEC	CS	CS		DSP	DSP Lab				
ECE4	SG4	AEC		CS	DLD	DLD	DSP LAB			DLD	DLD	DLD		IOT Lab	PSP	PSP	PSP		AEC				DSP					AEC Lab	AEC		DSP	DSP		DLD Lab		AEC		
ECE5	SG5		AEC	AEC	CS	PSP	PSP		DLD	DLD	AEC	CS	CS		DLD	DLD		AEC		AEC Lab				DLD Lab			DSP	DSP		DSP Lab		DSP		DSP				
ECE6	ST2	AEC	PSP	DLD	DLD		DLD Lab		AEC	CS	CS		DSP	DSP		AEC	CS			AEC Lab			AEC Lab		AEC		DSP Lab	PSP	PSP	DLD	DLD		DSP	DSP				

Counselling

S.NO	Subject Code	Subject Name	Faculty Name
1	20MA2101	Probability & Random Variables	Mr.N.Mohan Rao(1,2,3,4,5),TSH(1)
2	20EC2185	Verilog of Things Lab	Mr.P.vinod Babu(ECE3,ECE4,CEE6)&Mr.K.Suresh(ECE1,ECE2,ECE5)
3	20EC2101	Analog Electronic Circuits	Mr.Sk.Irfan ali(ECE5,ECE6)&Mr.Rajkumar(ECE1,2,3,4)
4	20EC2181	Analog Electronic Circuits Lab	Mr.Sk.Irfan ali(ECE2,5,6),Mr.Rajkumar(ECE4,3)&Mr.Sk.Karimulla (ECE1)
5	20EC2102	Digital Logic Design	Mr.P.Shyam(ECE4,1,5),Mr.J.Prasad(ECE3,ECE2)&K.VENKATESWARA RAO (ECE6)
6	20EC2182	Digital Logic Design Lab	Mr.P.Shyam(ECE1,2,4)&Mr.K.Venkateswara rao(ECE3,5,6)
7	20EC2103	Digital Signal Processing	Mr.K.Sivalaji(ECE1,2,3)&Mr.Charles Stud(ECE5,6,3)
8	20EC2183	Digital Signal Processing Lab	Mr.K.Sivalaji(ECE1,2,3),Mrs.Y.Priyanaka (ECE5,4,6)
9	20EE21XX	Control Systems	Mr.J.Chakravarthi(ECE1,ECE3)

Mr.Jagadeesh (ECE5,6) 9666930162
Madhuri (ECE2,A) 9392832668

P1:8.30am - 9.30am
P2:9.30am - 10.30am
P3:10.30am - 11.30am
P4:11.30am to 12.30pm
P5:2.00PM-3.00PM
P6:3.00PM-4.00PM
P7:4.00-5.00PM

AEC LAB	AB1 F1 Back side	Mrs.V.GanagaBhavani, Mrs.J.L.Prasanna
DSP Lab	AB2 Block Third Floor DSP Lab	Mr.J.Venkatesh&Mr.Jyalkalshmi
DLD Lab	AB2 Block F1 Back Side	Mr.Y.Subramanyam,Mrs.M.Suneetha&Mrs.S
IOT Lab	AB2 Block S9 & S10 Back Side	Mr.B.Siva Rama krishna
DLD Lab (CS1)	AB2 Block F1 Back Side	

Faculty
Mr.Raj Kumar Jaiswal
Mr.J.prasad
Mr.A.Charles Stud

Class Room	Class Code
SG1	ECE1
SG2	ECE2
SG3	ECE3
SG4	ECE4
SG5	ECE5
ST2	ECE6