

## Department of Electrical & Electronics Engineering

### EE E1-SEM-2 TIMETABLE( A.Y-22-23)

	Time	8.30-9.30	9.30 -10.30	10.30-11.30	11.30-12.30	L U N C H  B R E A K	1.30-2.30	2.30- 3.30	3.30 -4.30	Type	S.CODE		SUBJECT	CREDITS	FACULTY NAME
		P1	P2	P3	P4		P5	P6	P7						
EEE-E1-01 AB-III TG- 8	Monday	MM	MM	NT	NT	L U N C H  B R E A K	CP LAB(Mrs.shravani,Dr.jyothilal nayak)			BSC	22MA1201	MM	Mathematical Methods	4	Mr.A.Sudhir kumar
	Tuesday	Int to AI		EDC	EDC		DLD	DLD		PCC	22EC2102	DLD	Digital Logic Design	4	Mr.Sivalal
	Wed	MM	MM	NT	NT		DLD LAB(Mr.KVR )			PCC-1	22EC2182	DLD LAB	Digital Logic Design Lab	1.5	Mr.K.Ventateswara rao, Mr.Ratnakar,Mrs.Shravani,Dr.J yothilal Nayak
	Thu			DLD	DLD					BSC-1	22EE1281	CP LAB	Computational Lab	1.5	
	Fri		EDC LAB(Durga bhavani)							HSC	22EG1281	Eng Lab	English Language Communication Skills Lab 1	2.5	Mr.Katyayini
	Sat	MM	MM							ESC	22EC1201	EDC	Electronics Devices and Circuits	4	P.Vinod babu,B.Venkata ramana
										ESC-1	22EC1281	EDC LAB	Electronics Devices and Circuits Lab	1.5	Mr.venkeswara rao, Mr.durga bavani
								PCC-2	22EE1201	NT	Network Theory	4	Mr.Madhusudhan Rao		
									BSC-2	22EE1202	Int to AI	Introduction to AI	1	Mr.J.Chakravarthy	

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		P1	P2	P3	P4		P5	P6	P7
EEE-E1-02 AB-II ST5	Monday	Int to AI		DLD	DLD	L U N C H  B R E A K			
	Tuesday	MM	MM	NT	NT		CP LAB(Mrs.Shravani,Mr.Ratnakar)		
	Wed	EDC	EDC	DLD	DLD				
	Thu	MM	MM	NT	NT				
	Fri	MM	MM	EDC	EDC		DLD LAB(Mr.KVR)		
	Sat		ENG LAB(Mrs.Katyayini Mam)						
							EDC LAB(Mr.KVR)		